



DS-03-009

October 24, 2003

To: Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572  
28 Davis Avenue  
Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/627,797 07/25/03

Dr. Achim Stellberger

PROTECTION SWITCH WITH REVERSE  
VOLTAGE PROTECTION

Grp. Art Unit:

#### INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation  
In An Application.

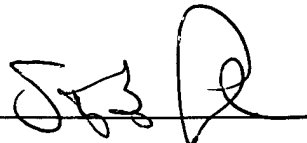
The following Patents and/or Publications are submitted to  
comply with the duty of disclosure under CFR 1.97-1.99 and  
37 CFR 1.56. Copies of each document is included herewith.

#### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being  
deposited with the United States Postal Service as first class  
mail in an envelope addressed to: Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450, on October 27, 2003.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

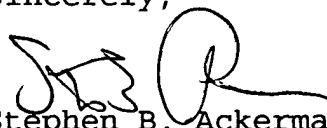
 10/27/03

U.S. Patent 5,877,534 to Williams et al., "Method of Forming Electrostatic Discharge Protection Device for Integrated Circuit," describes a method of forming an electrostatic discharge protection device for integrated circuits, where an electrostatic discharge (ESD) device includes a pair of depletion mode MOSFETs connected drain-to-drain in a series path between an input terminal and an output terminal, the gate of each MOSFET being connected to its source.

U.S. Patent 5,939,908 to Moore et al., "Dual FET Driver Circuit," discloses a dual FET driver circuit for supplying an electric current to a device having a pair of power FET's connected in series between the device and a power supply.

U.S. Patent 6,154,081 to Pakkala et al., "Load Circuit Having Extended Reverse Voltage Protection," discloses a load circuit having extended reverse voltage protection, where an improved reverse voltage protection circuit that protects sensitive electronic devices from damage due to both reversed battery connections and source voltage interruptions.

Sincerely,

  
Stephen B. Ackerman,  
Reg. No. 37761

INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

DS-03-009

10/627,797

Dr. Achim Stellberger

07/25/03

Group Art Unit

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Portion or Pages, Etc.)

[illegible]

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.